



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TAKEMORI, Toshiyuki et al.

Serial No.: **09/660,439**

Filed: **September 12, 2000**

For: **TRANSISTOR AND METHOD OF MANUFACTURING THE SAME**

Group Art Unit: **2814**

Examiner: **Shrinivan (Steven) H. RAO**

P.T.O. Confirmation No.: **6603**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 3, 2003

Sir:

In response to the Office Action dated September 4, 2003, extended to October 3, 2003 by a **one** month Petition for Extension of Time, please favorably consider the applicants' remarks as follows:

Remarks/Arguments begin on page 2 of this paper.

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U.S. PATENT AND TRADEMARK OFFICE
OCT 14 2003